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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/832,685	04/10/2001	Srinivas Reddy	15114045720	2615
26059	7590 01/24/2002		_	
TOWNSEND AND TOWNSEND AND CREW LLP/ 015114			EXAMINER	
TWO EMBA 8TH FLOOR	RCADERO CENTER		CHANG, DANIEL D	
SAN FRANCISCO, CA 94111-3834			ART UNIT	PAPER NUMBER
			2819	
			DATE MAILED: 01/24/2002	

Please find below and/or attached an Office communication concerning this application or proceeding.

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· .	Application No.	Applicant(s)				
Office Action Summany	09/832,685	REDDY ET AL.				
Office Action Summary	Examiner	Art Unit				
The MAN INC DATE of this communication con	Daniel D. Chang	2819				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).  - Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).  Status						
1) Responsive to communication(s) filed on 24 S	September 2001 .					
2a) This action is <b>FINAL</b> . 2b) This action is non-final.						
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims						
4)⊠ Claim(s) <u>33-54</u> is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>33-54</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or election requirement.						
Application Papers						
9)☐ The specification is objected to by the Examiner.						
10)⊠ The drawing(s) filed on <u>10 April 2001</u> is/are: a)⊠ accepted or b)⊡ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
11)☐ The proposed drawing correction filed on is: a)☐ approved b)☐ disapproved by the Examiner.						
If approved, corrected drawings are required in reply to this Office action.						
12)☐ The oath or declaration is objected to by the Examiner.						
Priority under 35 U.S.C. §§ 119 and 120						
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
a) ☐ All b) ☐ Some * c) ☐ None of:						
1. Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No						
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.						
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).						
a) ☐ The translation of the foreign language provisional application has been received.  15) ☑ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.						
Attachment(s)						
Notice of References Cited (PTO-892)     Notice of Draftsperson's Patent Drawing Review (PTO-948)     Information Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Informa	ary (PTO-413) Paper No(s) al Patent Application (PTO-152)				

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## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

Claims 33-54 are rejected under 35 U.S.C. 102(b) as being anticipated by Goetting et al. (US 5,365,125).

Goetting et al. discloses in figures 3, 4 and 20, a programmable interconnect bus (I1, I2); a plurality of logic elements (300, 310, 320, 330, 349, CCUs in CELL\_1-CELL\_9) configurable to perform logical functions; a plurality of tristate devices (341) coupled between the plurality of logic elements and the programmable interconnect bus; a plurality of programmable memory cells (see CCU4, CCU5 in fig. 20B; see col. 6, lines 34-36) coupled to the plurality of tristate devices to programmably enable and programmably tristate the plurality of tristate devices; and tristate control logic (output of 304 in fig. 20B) coupled to the plurality of tristate devices to dynamically enable and dynamically tristate the plurality of tristate devices.

Claims 33-54 are rejected under 35 U.S.C. 102(e) as being anticipated by Tavana et al. (US 5,682,107).

Tavana et al. discloses in figures 3A, 4A-4C, a programmable interconnect bus (TQ4-TO7); a plurality of logic elements (F-J) configurable to perform logical functions; a plurality of

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tristate devices (B4-B7) coupled between the plurality of logic elements and the programmable interconnect bus; a plurality of programmable memory cells (MM4-MM7) coupled to the plurality of tristate devices to programmably enable and programmably tristate the plurality of tristate devices; and tristate control logic (OR1) coupled to the plurality of tristate devices to dynamically enable and dynamically tristate the plurality of tristate devices.

## **Double Patenting**

The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

Claims 33-54 are rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-48 of U.S. Patent No. 6239613 (hereinafter, '613). Although the conflicting claims are not identical, they are not patentably distinct from each other because claims 33-54 of the instant application and claims 1-48 of '613 recite tristate control logic coupled to the plurality of tristate devices to dynamically enable and dynamically tristate the plurality of tristate devices. The difference between claims 33-54 of the present application and claims 1-48 of '613 is that claims of the present application is only broader.

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## Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel D. Chang whose telephone number is (703) 306-4549. The examiner can normally be reached on Monday through Thursday and every other Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael J. Tokar can be reached on (703) 305-3493. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9318 for regular communications and (703) 872-9319 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Daniel D. Chang

Examiner

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DC

January 22, 2002